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23 (New) The method of claim 1, wherein said two lands are electrically coupled each other by a through hole formed therein.

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23 (New) The method of claim 1, wherein the capacitors are substantially identical each other.

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24 (New) The method of claim 1, wherein the capacitors are connected in parallel.

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25 (New) The method of claim 1, wherein voltages applied to the capacitors are varied.

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26 (New) The method of claim 1, wherein voltages applied to the capacitors have frequencies varying in an audible frequency band.

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27 (New) A method for mounting multilayered ceramic capacitors on a circuit board having a front surface and a back surface, wherein each capacitor includes a body having dielectric layers formed of a dielectric ceramic material and internal electrode layers and a pair of external terminal electrodes formed on two sides of the body, the dielectric layers and the internal electrode layers being stacked alternately in the body and the internal electrode layers being connected in parallel to the external terminal electrodes in an alternate manner, the method comprising the step of:

20 mounting the capacitors on substantially plane-symmetrical positions of the front and the back surfaces, respectively, wherein the capacitors are substantially identical each other and substantially identical voltages are applied to the capacitors.

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(New) The method of claim 7, wherein said mounting step includes the steps of:  
forming lands at substantially plane-symmetrical positions on the front and the back  
surfaces, wherein every two lands disposed at their substantially plane-symmetrical positions  
are connected each other; and

5 mounting the capacitors on the lands of the front and the back surfaces such that the  
external terminal electrodes of the capacitors are electrically coupled to the lands on the front  
and the back surfaces.

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10 (New) The method of claim 8, wherein said two lands are electrically coupled each  
other by a through hole formed therein.

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(New) The method of claim 7, wherein the capacitors are substantially identical  
each other.

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15 (New) The method of claim 7, wherein the capacitors are connected in parallel.

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(New) The method of claim 7, wherein voltages applied to the capacitors are varied.

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20 (New) The method of claim 7, wherein voltages applied to the capacitors have  
frequencies varying in an audible frequency band.

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25 (New) A method for mounting multilayered ceramic capacitors on a circuit board  
having a front and a back surfaces, the capacitors being used in an electronic circuit as  
components thereof and voltages applied to the capacitors being varied, wherein each  
capacitor includes a body having dielectric layers formed of a dielectric ceramic material and

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internal electrode layers and a pair of external terminal electrodes formed on two sides of the body, the dielectric layers and the internal electrode layers being stacked alternately in the body and the internal electrode layers being connected in parallel to the external terminal electrodes in an alternate manner, the method comprising the steps of:

5 forming lands at substantially plane-symmetrical positions on the front and the back surfaces, wherein every two lands disposed at their substantially plane-symmetrical positions are connected each other; and

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10 mounting the capacitors on the lands of the front and the back surfaces such that the capacitors are disposed at substantially plane-symmetrical positions and the external terminal electrodes of the capacitors are electrically coupled to the lands on the front and the back surfaces, wherein the capacitors are substantially identical each other and substantially identical voltages are applied to the capacitors.

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15. (New) The method of claim 14, wherein said two lands are electrically coupled each other by a through hole formed therein.

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16. (New) The method of claim 14, wherein the capacitors are substantially identical each other.

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20 17. (New) The method of claim 14, wherein the capacitors are connected in parallel.

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18. (New) The method of claim 14, wherein voltages applied to the capacitors are varied.

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25 19. (New) The method of claim 14, wherein voltages applied to the capacitors have frequencies varying in an audible frequency band.

Respectfully submitted,



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